

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Appl. No.	:	10/708,056	Confirmation No.:	2055
Applicant:	:	Edmund M. Clarke, et al.		
Filed:	:	February 5, 2004		
Title:	:	Method and System to Verify a Circuit Design		
TC/A.U.	:	2825		
Examiner:	:	Vuthe Siek		
Docket No.	:	361007-000031		
Customer No.	:	24,239		

Mail Stop: Amendment  
Commissioner for Patents  
PO Box 1450  
Alexandria, VA 22313-1450

***AMENDMENT UNDER 37 CFR §1.111***

Sir:

In response to the Office Action dated as mailed July 24, 2006, please amend the above-identified application as follows:

**Amendments to the Specification** are reflected on page 2.

**Amendments to the Claims** are reflected in the listing of claims which begins on page 4 of this paper.

**Remarks** begin on page 12 of this paper.